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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

## TRANSMITTAL LETTER TO THE UNITED STATES

215225US2PCT

DESIGNATED/ELECTED OFFICE (DO/EO/US)

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR

CONCERNING A FILING UNDER 35 U.S.C. 371

09/926399

INTERNATIONAL APPLICATION NO.

PCT/JP00/02658

INTERNATIONAL FILING DATE

24 April 2000

PRIORITY DATE CLAIMED

28 April 1999

TITLE OF INVENTION

MANUFACTURING METHOD OF FLAT PANEL DISPLAY, AND MANUFACTURING EQUIPMENT OF FLAT PANEL DISPLAY

APPLICANT(S) FOR DO/EO/US

Takashi ENOMOTO, et al.

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (24) indicated below.
4. The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
  - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
  - b. ☒ has been communicated by the International Bureau.
  - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
  - a. ☒ is attached hereto.
  - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
  - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
  - b. ☐ have been communicated by the International Bureau.
  - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
  - d. ☒ have not been made and will not be made.
8. An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).
10. ☐ An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).
11. ☒ A copy of the International Preliminary Examination Report (PCT/IPEA/409).
12. ☒ A copy of the International Search Report (PCT/ISA/210).

## Items 13 to 20 below concern document(s) or information included:

13. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☐ A **FIRST** preliminary amendment.
16. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
17. ☐ A substitute specification.
18. ☐ A change of power of attorney and/or address letter.
19. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
20. ☐ A second copy of the published international application under 35 U.S.C. 154(d)(4).
21. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
22. ☐ Certificate of Mailing by Express Mail
23. ☒ Other items or information:

PC/IB/304

Drawings (4 Sheets)

PCT/IB/308

Notice of Priority

Request for Consideration of Documents Cited in the International Search Report

26 OCT 2001

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 1.53) <b>09/926399</b>		INTERNATIONAL APPLICATION NO. <b>PCT/JP00/02658</b>		ATTORNEY'S DOCKET NUMBER <b>215225US2PCT</b>	
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24. The following fees are submitted: <b>BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :</b> <input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO ..... <b>\$1040.00</b> <input checked="" type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO ..... <b>\$890.00</b> <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO ..... <b>\$740.00</b> <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) ..... <b>\$710.00</b> <input type="checkbox"/> International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) ..... <b>\$100.00</b> <b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>				<b>CALCULATIONS PTO USE ONLY</b>  <div style="border: 1px solid black; height: 100px; width: 100%;"></div>	
				<b>\$890.00</b>	
Surcharge of <b>\$130.00</b> for furnishing the oath or declaration later than _____ months from the earliest claimed priority date (37 CFR 1.492 (e)). <input type="checkbox"/> 20 <input checked="" type="checkbox"/> 30				<b>\$130.00</b>	
CLASS	NUMBER FILED	NUMBER EXTRA	RATE		
Total	14 - 20 =	0	x \$18.00	<b>\$0.00</b>	
Independent claims	2 - 3 =	0	x \$84.00	<b>\$0.00</b>	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>				<b>\$0.00</b>	
<b>TOTAL OF ABOVE CALCULATIONS =</b>				<b>\$1,020.00</b>	
<input type="checkbox"/> Small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.				<b>\$0.00</b>	
<b>SUBTOTAL =</b>				<b>\$1,020.00</b>	
Surcharge of <b>\$130.00</b> for furnishing the English translation later than _____ months from the earliest claimed priority date (37 CFR 1.492 (f)). <input type="checkbox"/> 20 <input type="checkbox"/> 30 +				<b>\$0.00</b>	
<b>TOTAL NATIONAL FEE =</b>				<b>\$1,020.00</b>	
Fee for filing the enclosed assignment (37 CFR 1.21(h)). The assignment must be filed by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable). <input type="checkbox"/>				<b>\$0.00</b>	
<b>TOTAL FEES ENCLOSED =</b>				<b>\$1,020.00</b>	
				Amount to be: refunded \$	
				charged \$	

a. A check in the amount of **\$1,020.00** to cover the above fees is enclosed.

b. Please charge my Deposit Account No. \_\_\_\_\_ in the amount of \_\_\_\_\_ to cover the above fees. A duplicate copy of this sheet is enclosed.


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d. ☐ Fees are to be charged to a credit card. **WARNING:** Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

**NOTE:** Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

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**22850**

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**24,913**  
REGISTRATION NUMBER

Oct 26 2001  
DATE

MANUFACTURING METHOD OF FLAT PANEL DISPLAY, AND MANUFACTURING  
EQUIPMENT OF FLAT PANEL DISPLAY

## Technical Field

5    **[0001]** The present invention relates to a manufacturing method and manufacturing equipment of a flat panel display, which has an electron emitting element, such as a field emission cold cathode or the like.

## Background

20 [0002] Recently, by making use of highly developed semiconductor machining techniques, a field emission cold cathode is under active development, and an application of the field emission cold cathode to a flat panel (planar type) display is under progress. A flat panel display having a filed emission type electron-emitting element is self-emitting type, different from a liquid crystal display, and a backlight is unnecessary. Accordingly, there are various advantages that low power consumption may be realized, a broader field angle may be obtained, and a rapid response speed may be obtained.

[0003] As the flat panel display like this, one that has a structure, such as shown in Figs. 7A and 7B, is known. Fig. 7B is a sectional view showing, in enlargement, a portion surrounded by a circle in Fig. 7A.

25    **[0004]** In this image display, a silicon dioxide film 103, which has a large number of cavities 102, is formed on a Si substrate 101 as a rear plate, and, a gate electrode 104, consisting of molybdenum or niobium, is formed on the silicon dioxide film 103.

A field emission type electron emitting element 105, consisting of cone-like molybdenum or the like, is formed on the Si substrate 101 inside the cavities 102.

[0005] A transparent substrate (face plate) 106, consisting of a glass substrate or the like, is disposed in parallel with the Si substrate 101 like this, which has a large number of electron emitting elements 105, so as to face the Si substrate 101 with a predetermined spacing. Therefrom, a vacuum envelope 107 is configured. A phosphor screen 108 is formed on a surface facing the electron emitting elements 105 of the transparent substrate 106. In addition, in order to sustain an atmospheric pressure on the Si substrate 101 and the transparent substrate 106, supporting members 109 are disposed between these substrates.

[0006] In the aforementioned flat panel display, electron beams, emitted from a large number of electron emitting elements 105, are illuminated on the phosphor screen 108; the phosphor screen 108 is excited to emit light; and, thereby, an image is formed. In the image display like this, the electron emitting elements 105 may be formed in a small size of micrometer-order, and the spacing between the Si substrate 101 and the transparent substrate 106 may be formed in a size of millimeter-order. As a result, in the flat panel display, higher resolution, lighter weight, and thinner thickness may be attained, in comparison with cathode ray tubes (CRT) which have been used for television sets or computer displays.

[0007] In the flat panel display having the aforementioned structure, a vacuum degree inside the device is necessary to be maintained in the range of, for instance,  $10^{-7}$  to  $10^{-8}$  Torr. In

order to attain this, in the existing exhausting process, gas adsorbed on an inside surface of the image display is degassed in the shortest time, by applying baking treatment, in which the image display is heated up to approximately 350°C. However, by such an exhausting method, the gas adsorbed on the surface cannot be sufficiently degassed.

[0008] On the other hand, in the existing CRTs and so on, a getter disposed inside the device is activated after sealing, and, the gas released from an inner wall, during operation, is absorbed by the getter, thereby a desired degree of vacuum is maintained. This technique for obtaining a high vacuum and maintaining a degree of vacuum by means of gettering material is under way in applying in the flat panel displays.

[0009] In the flat panel display in which the field emission electron emitting elements are used, while a volume of a vacuum vessel (vacuum envelope), which is determined by the rear and face plates and the supporting frame disposed at the sides thereof, may be largely reduced in comparison with that of the ordinary CRT, an area of an inner surface, from which the gas is released, is not so much reduced. As a result, when the surface adsorption gas is released to an extent equivalent with that of the CRT, an increase in pressure in the vacuum vessel becomes remarkably large. Accordingly, in the flat panel display, role of the gettering material becomes very important. However, a place, where a conductive gettering film is formed, has been limited, from a viewpoint of inhibiting short circuits of interconnections.

[0010] To such problems, it is proposed to dispose the gettering material at a position, other than an image display region, of

204 0013] Therefore, in the above publication, it is disclosed that,  
after a space between the faceplate and the rear plate is  
hermetically sealed through supporting frames, and a vacuum  
envelope is formed, the gettering material is activated by means  
5 of electron beam irradiation and so on. However, according to  
such method, the gettering material may not be effectively  
activated. In particular, in case the gettering material is  
activated, after the vacuum envelope is formed, since gas  
components, such as oxygen or the like, which are released due  
10 to the activation, are adsorbed by the electron emitting elements  
or other members, electron emissivity or the like may be  
deteriorated.

0014] The present invention is carried out to overcome these  
problems. The object of the present invention is to provide a  
15 method for manufacturing a flat panel display, according to which,  
gas adsorbed on an inside surface of the device in the course of  
manufacturing process, may be sufficiently degassed, and, thereby,  
a high vacuum state may be maintained inside the vacuum vessel  
(the vacuum envelope); and manufacturing equipment of the flat  
20 panel displays.

#### Disclosure of the Invention

0015] A first aspect of the present invention is a manufacturing  
method of a flat panel display. The manufacturing method of a  
25 flat panel display comprises joining a substrate, which has an  
electron emitting element, and a faceplate, which has a phosphor  
screen, so that the electron emitting element and the phosphor  
screen face to each other with a gap and irradiating electrons

the vacuum vessel, and to form the gettering film, in a periphery portion that does not affect on the image display region (Japanese Patent Laid -Open Application No. 5-151916 JP-A, Japanese Patent Laid -Open Application No. 4-289640 JP-A, and so on). However, according to such methods, the gas generated in the image display region may not be effectively absorbed by the gettering film, which is formed in the periphery portion. As a result, there has been a problem that it is difficult to maintain a high vacuum in the vacuum envelope, over a long time period.

[0011] By the above reasons, it is under study to dispose the gettering film in the image display region. In Japanese Patent Laid -Open Application No. 9-82245 JP-A, for instance, the following is disclosed. That is, a gettering material, consisting of titanium (Ti), zirconium (Zr), or alloy thereof, is sputtered on a metal back layer, which is formed on a phosphor film in a faceplate of a flat panel display; or the metal back layer is constituted of the aforementioned gettering material; or the above gettering material is disposed on a portion, other than the electron emitting element, of the rear plate, in the image display region.

[0012] However, in the flat panel display, which is disclosed in the above Japanese Patent Laid -Open Application No. 9-82245 JP-A, since the gettering material is formed according to an ordinary panel formation process, a surface of the gettering material is naturally oxidized. In the gettering material, since activity of the surface thereof is very important, the surface-oxidized gettering material could not obtain a sufficient gas absorption effect.

onto at least one of the substrate and the faceplate, in a vacuum atmosphere.

[0016] More specifically, the irradiating of electrons has accommodating at least one of the substrate and the faceplate in a treatment vessel, and irradiating the electrons onto at least one of the substrate and the faceplate accommodated in the treatment vessel from one or more electron sources disposed therein.

[0017] In the present manufacturing method of the flat panel display, the electrons are preferably irradiated in a vacuum atmosphere of which degree of vacuum is maintained at  $10^{-3}$  Torr or less in the irradiating of electrons. In addition, in the electron irradiating process, it is preferable to irradiate the electrons onto at least one of the substrate and the faceplate, while being heated. In heating, at least one of the substrate and the faceplate is preferably heated to a temperature from 200 to 400°C. Furthermore, after the electrons are irradiated, an irradiated object is preferably cooled to a temperature of 100°C or less. After the irradiating of electrons, the substrate and the faceplate may be joined through a supporting frame in a vacuum atmosphere. The supporting frame may be irradiated with the electrons.

[0018] A second aspect of the present invention is a manufacturing equipment of a flat panel display. The manufacturing equipment of a flat panel display comprises a treatment vessel in which at least one of a substrate, which has an electron emitting element, and a faceplate, which has a phosphor screen, is accommodated, transferring means for sending



at least one of the substrate and the faceplate in and out of the treatment vessel, exhausting means for evacuating the inside of the treatment vessel to a vacuum atmosphere, irradiating means for irradiating an electron beam onto at least one of the substrate and the faceplate, which are accommodated in the treatment vessel, and joining means for joining the substrate and the faceplate, at least one of which is irradiated with the electron beam, while arranging so as for the electron emitting element and the phosphor screen to face to each other with a gap.

[0019] The manufacturing equipment of a flat panel display of the present invention may further include means for heating at least one of the substrate and the faceplate, which are accommodated in the treatment vessel.

[0020] In general, the irradiation of the electron beam onto a solid material may detach gas adsorbed on a solid surface. Accordingly, by accommodating the substrate, which has the electron emitting elements, or the faceplate, in the treatment vessel, the inside of which is evacuated to be a vacuum atmosphere, and by irradiating the electron beam onto the substrate or the faceplate, from an electron source disposed in the treatment vessel, an entire surface of the substrate and of the faceplate may undergoes thorough electron beam cleaning, and, thereby, surface adsorbed gas may be sufficiently released. By performing such electron beam irradiation, the inside of the vacuum vessel, which constitutes the envelope of the flat panel display, may be made capable of maintaining a high vacuum state, for instance, a degree of vacuum of from  $10^{-7}$  to  $10^{-8}$  Torr.

## Brief Description of the Drawings

[0021] Figs. 1A and 1B are sectional views showing schematically manufacturing processes according to one embodiment, in a manufacturing method of a flat panel display of the present invention.

[0022] Fig. 2 is a diagram showing roughly one example of a configuration of vacuum treatment equipment, which is used in the manufacturing method of the present invention.

[0023] Fig. 3 is a sectional view showing, in enlargement, one example of a structure of a faceplate end portion, in the manufacturing method of the flat panel display of the present invention.

[0024] Fig. 4 is a diagram showing schematically a first example of an electron beam cleaning process, in the manufacturing method of the flat panel display of the present invention.

[0025] Fig. 5 is a diagram showing schematically a second example of an electron beam cleaning process, in the manufacturing method of the flat panel display of the present invention.

[0026] Fig. 6 is a diagram showing schematically a third example of an electron beam cleaning process, in the manufacturing method of the flat panel display of the present invention.

[0027] Figs. 7A and 7B are sectional views showing a structure of an essential portion of the flat panel display.

## Embodiments

[0028] In the following, embodiments of the present invention are described. Incidentally, the present invention is not restricted to the following embodiments.

[0029] First, a manufacturing method of a flat panel display of the present invention will be explained, with reference to Fig. 1.

[0030] As shown in Fig. 1A, a faceplate 10, a rear plate 20, and support frames 30 are prepared according to an ordinary method.

[0031] The faceplate 10 has a transparent substrate, such as a glass substrate 11 or the like, and a phosphor layer 12 which is formed on the transparent substrate. In a color image display, the phosphor layer 12 has a red emitting phosphor layer, a green emitting phosphor layer, and a blue emitting phosphor layer, all of which are formed corresponding to pixels. In addition, a light absorbing layer 13, consisting of black conductive material, is disposed so as to separate between them. The phosphor layer 12, which emits in each of red color, green color, and blue color, and the light absorbing layer 13, which separates them, are repeatedly formed in turn in a horizontal direction. A phosphor screen is constituted of the phosphor layer 12 and the light absorbing layer 13, and the phosphor screen is an image display region.

[0032] The light absorbing layer 13 is called black stripe, black matrix, and so on, according to its pattern. In the black stripe type phosphor screen, phosphor stripes of red, green, and blue colors are formed in turn, and, the light absorbing layer 13, which is formed in stripes, separates between the phosphor stripes. In the black matrix type phosphor screen, phosphor dots of red, green, and blue colors are formed in lattice, and the light absorbing layer 13 separates between them. To arrange the phosphor dots, various kinds of methods may be applied.

[0033] A metal back layer 14 is formed on the phosphor layer 12.

The metal back layer 14, which is constituted of a conductive thin film, such as Al film or the like, reflects light proceeding toward

a rear plate 20, which has an electron emitting source, of light

generated by the phosphor layer 12, and thereby, may improve

brightness. Furthermore, the metal back layer 14 endows

conductivity the image display region of the faceplate 10, thereby,

may suppress charges from accumulating, and plays the role of an

anode electrode with respect to the electron emitting source of

the rear plate 20. Still furthermore, the metal back layer 14

has a function that inhibits ions of residual gas generated in

the vacuum vessel (envelope) by the electron beam from the

electron emitting source, from damaging the phosphor layer 12.

[0034] Slurry method and printing method may be applied, as the

method for forming the phosphor layer 12 and the light absorbing

layer 13 on the glass substrate 11. After the phosphor layer 12

and the light absorbing layer 13 are formed on the glass substrate

11, respectively, further thereon, a conductive thin film,

consisting of Al film or the like, is formed by means of vapor

deposition or sputtering, and thereby the metal back layer 14 is

formed. The thickness of the Al film is, though dependent on an

anode voltage or the like, preferable to be 2500 nm or less.

[0035] The rear plate 20 has a substrate 21, such as an insulating

substrate, such as glass substrate, ceramic substrate or a silicon

(Si) substrate and a large number of electron emitting elements

22 on the substrate 21. These electron emitting elements 22

comprise, for instance, field emission cold cathodes (emitter)

or surface conduction electron emitting elements. Wiring (not

shown) is disposed on a surface of the rear plate 20, on which electron emitting elements 22 of are formed. That is, the number of electron emitting elements 22 are formed in matrix, corresponding to phosphors of individual pixels, and the wiring (X-Y wiring), which intersect with each other, are formed to drive the matrix-like electron emitting elements 22, line by line.

[0036] The supporting frames 30 hermetically seal a space between the faceplate 10 and the rear plate 20. The supporting frames 30 are joined to the faceplate 10 and the rear plate 20, by means of frit glass or indium (In) or alloy thereof. These form the vacuum vessel, as the envelope described below. The supporting frames 30 are provided with signal input terminals and signal line selection terminals (not shown). These terminals correspond to the cross wiring (X-Y wiring) of the rear plate 20.

[0037] When the flat panel display is large in size, bending may be caused, since the device is formed in a thin plane table. In order to avoid the bending and increase mechanical strength against the atmospheric pressure, reinforcement plates (atmospheric pressure sustaining member) 15 may be appropriately disposed, as shown in Fig. 1B, in conformity with an intended strength.

[0038] The above-mentioned faceplate 10, rear plate 20 and supporting frames 30 are prepared, respectively. Thereafter, the cleaning of the substrate by means of the electron beam irradiation, the vacuum deposition of the getter film, and the formation of the vacuum vessel as the envelope (joining the supporting frames 30 to the faceplate 10 and rear plate 20) are performed, while maintaining the vacuum atmosphere. For such

sequence of the process, vacuum treatment equipment 40, as shown, in Fig. 2, may be used.

[0039] The vacuum treatment equipment 40 shown in Fig. 2 includes a loading chamber 41 of the faceplate 10, a baking and electron beam cleaning chamber 42, a cooling chamber 43, a vapor deposition chamber of the getter film 44, a loading chamber 45 of the rear plate 20 and the supporting frames 30, a baking and electron beam cleaning chamber 46, a cooling chamber 47, an assembly chamber 48 of the faceplate 10 and the rear plate 20, a heat treatment chamber 49 for joining the supporting frames 30 to the faceplate 10, a cooling chamber 50, and an unloading chamber 51.

[0040] Each of the aforementioned treatment chambers (treatment vessels) is a vacuum treatment chamber in which the processing in the vacuum atmosphere may be applied, and, all chambers are evacuated, when the image displays are manufactured. The degree of vacuum, is preferable to be, for instance,  $1 \times 10^{-3}$  Torr or less, furthermore to be  $1 \times 10^{-5}$  Torr or less. The individual treatment chambers are connected by gate valves or the like. In addition, the vacuum treatment equipment 40 includes means for sending in and out the faceplate 10 and the rear plate 20, to be treated, and for transferring them between the Individual treatment chambers (not shown), and vacuum exhausting means (exhausting device or the like) for exhausting the inside of the individual treatment chambers (not shown).

[0041] The faceplate 10, which has undergone up to the formation of the metal back layer 14, is first set inside the loading chamber 41. At end portions of the faceplate 10, grooves 31 are formed beforehand, as shown in Fig. 3, and, joining material 32, such

as In or alloy thereof, may be disposed in the grooves 31, so as to hermetically seal between the faceplate 10 and the supporting frames 30. Then, after the atmosphere in the loading chamber 41 is evacuated to be a vacuum atmosphere, the faceplate 10 is sent  
5 in the baking and electron beam cleaning chamber 42.

[0042] In the baking and electron beam cleaning chamber 42, the faceplate 10 is heated up to a temperature in the range from 300 to 400°C, for instance, and the faceplate 10 is degassed. When the joining material 32, is previously disposed in the grooves  
10 31 at the end portions of the faceplate 10, the joining material 32 may drop out of the grooves 31 after melting due to heating. In order to keep the joining material 32 from dropping, the faceplate 10 is preferably disposed at a lower portion in the baking and electron beam cleaning chamber 42, with the grooves  
15 31 directed upward.

[0043] Simultaneously with the aforementioned baking, as shown in Fig. 4, for instance, an electron beam 53 is irradiated onto a surface having the phosphor screen of the faceplate 10, in the vacuum atmosphere, from an electron beam generator 52, which is  
20 disposed at an upper portion of the baking and electron beam cleaning chamber 42. The degree of vacuum, at the irradiation of the electron beam 53, is preferable to be  $1 \times 10^{-3}$  Torr or less, furthermore preferable to be  $1 \times 10^{-5}$  Torr or less. The electron beam 53 is deflected and scanned by a deflection yoke 54 attached  
25 to an outside of the electron beam generator 52. Thereby, an entire surface of the faceplate 10 may be irradiated by the electron beam, and cleansed.

[0044] The number of the electron beam generator 52, a shape

thereof, and an electron beam generating method thereof are not particularly restricted to ones shown in Fig. 4. For instance, a plurality of electron beam generators 52 (two sets in Fig. 5) may be disposed and, the electron beams 53 may be alternately or  
5 simultaneously irradiated from the plurality of electron beam generators 52. Furthermore, an electron beam generator 56, which generates parallel beams 55, may be used, as shown in Fig. 6.

[0045] The faceplate 10 to which the heating and the electron beam cleaning have been applied, is transferred to the cooling  
10 chamber 43, and, cooled down to a temperature of, for instance, 100°C or less (80 to 100°C, for instance). Then, the cooled faceplate 10 is transferred into the vacuum deposition chamber 44 of the getter film. In the vacuum deposition chamber 44, on the outside of, for instance, the phosphor layer 12, a film of  
15 active barium (Ba) (not shown) is vacuum deposited as the getter film. Thereafter, the faceplate 10 is transferred to the assembly chamber 48.

[0046] Due to easiness of the process, the rear plate 20, in which the electron emitting sources are disposed on the substrate, and  
20 the supporting frames 30 are preferably integrated in one body, before being set in the loading chamber 45. Then, after the atmosphere in the loading chamber 45 is evacuated to be a vacuum atmosphere, the rear plate 20 and the supporting frames 30 (alternatively, an integrated assembly) are transferred from the  
25 loading chamber 45 to the baking and electron beam cleaning chamber 46.

[0047] In the baking and electron beam cleaning chamber 46, similarly as the aforementioned faceplate 10, the rear plate 20



and the supporting frames 30 are heated to a temperature in the range from 300 to 400°C to degas, the rear plate 20. At the same time with this baking, the electron beam is irradiated from the electron beam generator, for instance, the electron beam

5 generators 52 and 56, shown in Figs. 4 through 6, which are attached to an upper portion of the baking and electron beam cleaning chamber 46. The electron beams are deflected and scanned by means of the deflection yokes 54, which are attached to the outside of the electron beam generators 52 and 56, and an entire surface of the rear plate 20 may be cleansed by the electron beam.

10 [0048] Then, after the baking and the electron beam cleaning, the rear plate 20 and the supporting frames 30 are transferred to the cooling chamber 47, and cooled to a temperature of 100°C or less (80 to 100°C, for instance). The cooled rear plate 20 and supporting frames 30 are transferred to the assembly chamber 15 48, similarly as the aforementioned faceplate 10.

[0049] The faceplate 10, the rear plate 20, and the supporting frames 30 are assembled (positioning) in the assembly chamber 48. When assembling, the reinforcement plates may be disposed between 20 the faceplate 10 and the rear plate 20, if necessary.

[0050] Then, the assembled one is transferred into the heat treatment chamber 49. In the heat treatment chamber 49, heat treatment is performed in the vacuum atmosphere, at a temperature according to the joining material 32; the faceplate 10 and rear 25 plate 20 are pressed through the supporting frames 30 and joined. If necessary, the electron emitting source is activated beforehand. Since each process up to the joining is performed in the vacuum atmosphere, a surface of the getter film (Ba film)

formed in the vapor deposition chamber 44 is suppressed from being contaminated by oxygen or carbon; an active state is maintained.

[0051] In case that In or alloy thereof is used as the joining material 32, the joining is performed under heating at

5 approximately 100°C. At the depression during the joining, ultrasound may be preferably applied to the joining portion or in the neighborhood thereof, so as to make more sufficient joining.

When the joining material 32, such as In or alloy thereof, is previously disposed in the grooves 31 at the end portions of the

10 faceplate 10, In or the alloy 31 thereof may drop from the grooves 32, after melting by the heating during the joining. In order to inhibit this from occurring, the faceplate 10 is preferably disposed at a lower portion of the heat treatment chamber 49, with the grooves 31 directed upward, and, the rear plate 20, to which  
15 the supporting frames 30 are fixed, is preferably disposed from an upper portion thereof and joined.

[0052] It is generally said that In or alloy thereof is insufficient in joining strength. However, in the flat panel display of the present invention, since the gap between the

20 faceplate 10 and the rear plate 20 is maintained in a vacuum, a sufficient joining strength may be obtained due to the addition of the atmospheric pressure, even when In or alloy thereof only is used as the joining material 32. In order to increase furthermore the strength at the joining portion, the joining  
25 portion may be strengthened by means of epoxy resin or the like.

[0053] Thus, a vacuum vessel, as the envelope, is formed with the faceplate 10, the rear plate 20, and the supporting frames 30. That is, the flat panel display 60, shown in Fig. 1B, is

manufactured, by hermetically sealing the space between the faceplate 10 and the rear plate 20 by means of the supporting frames 30. Thereafter, the flat panel display 60 is cooled to room temperature in the cooling chamber 50, and, taken out of the unloading chamber 51.

[0054] The vacuum treatment equipment 40, which is used to manufacture the flat panel display 60, may be equipment in which individual configurations from the loading chamber 41 to the unloading chamber 51 are combined. Whenever the vacuum atmosphere of the inside of the vacuum treatment equipment may be maintained, the configuration thereof is not particularly restricted. Furthermore, in the aforementioned embodiment, the faceplate 10 and the rear plate 20 are separately electron beam cleansed. However, both held with a predetermined spacing distanced from a tool may simultaneously undergo the electron beam cleaning.

[0055] According to the flat panel display 60, which is obtained by the aforementioned manufacturing method and manufacturing equipment, a high vacuum state of  $10^{-7}$  to  $10^{-8}$  Torr, which is necessary for obtaining sufficient electron emissivity, may be attained at an initial stage, with good reproducibility. This is because, in addition to the individual steps being performed in the vacuum atmosphere, the entire surfaces of the faceplate 10 and the rear plate 20 are thoroughly electron beam cleansed, and the gas adsorbed on the surfaces thereof are sufficiently degassed. That is, since the gas is hardly evolved during operation of the flat panel display 60, excellent emission properties may be obtained over a long period.

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[0056] Furthermore, in the aforementioned manufacturing process of the flat panel display 60 of the present invention, since hermetic sealing process is performed in the vacuum atmosphere, after manufacture, a process for exhausting the inside of the device becomes unnecessary, contrary to the manufacture of the conventional flat panel display. Accordingly, an exhausting configuration (tubing for evacuation, for instance) or an exhauster, which is indispensable in the conventional device, becomes unnecessary. Furthermore, since such tubing for evacuation becomes unnecessary, exhaust conductance becomes larger, and, an exhausting efficiency of the flat panel display becomes excellent.

[0057] The flat panel display 60 as mentioned above is used in TV display, based on, for instance, TV signals according to NTSC system. At this time, a signal input terminal and a line selection terminal and furthermore a high voltage terminal (all of them are not shown) are connected with an external electric circuit. In case conductive In or In alloy is used for the joining material 32, the joining material 32 may be used also as the terminals.

[0058] To each of the terminals, scanning signals are inputted to sequentially drive, line by line, electron emitting sources disposed in the flat panel display 60, that is, electron emitting elements 22, which is interconnected in matrix of M rows x N columns, and, furthermore, modulation signals, which control an output electron beam of the electron emitting elements 22 of the selected one line, are inputted. At the high voltage terminal, an accelerating voltage is inputted to endow the electron beam, which is emitted from the electron emitting elements 22, sufficient

energy to excite the phosphor.

[0059] In the flat panel display 60 configured thus, a voltage is applied to each of the electron emitting elements 22 through the terminal, and thereby electron emission is caused. In addition, a high voltage is applied through a high voltage terminal to the metal back layer 14, and thereby the electron beam is accelerated. The accelerated electrons impinge on the phosphor layer 12 and cause the phosphor layer to emit, and thereby an image is displayed.

[0060] The flat panel display obtained according to the present invention may be used as various kinds of image displays, such as, for instance, displays of TV receivers or computer terminals.

#### Industrial Applicability

[0061] As explained above, according to the manufacturing method and the manufacturing equipment of the flat panel displays of the present invention, the surface adsorbed gas may be sufficiently degassed, due to a thorough electron beam cleaning of the entire surface of the faceplate or the rear plate. Accordingly, the inside of the flat panel display may be maintained in high vacuum state, for a long time period.

What is claimed is:

1. A manufacturing method of a flat panel display,  
comprising:

joining a substrate, which has an electron emitting element,  
5 and a faceplate, which has a phosphor screen, so that the electron  
emitting element and the phosphor screen face to each other with  
a gap; and

irradiating electrons onto at least one of the substrate  
and the faceplate, in a vacuum atmosphere.

10 2. A manufacturing method of a flat panel display as set  
forth in claim 1,

wherein the irradiating of electrons has accommodating at  
least one of the substrate and the faceplate in a treatment vessel,  
and irradiating the electrons onto at least one of the substrate  
15 and the faceplate accommodated in the treatment vessel from one  
or more electron sources disposed therein.

3. A manufacturing method of a flat panel display as set  
forth in claim 2,

20 wherein the electrons are irradiated alternately or  
simultaneously from two or more electron sources, which are  
disposed in the treatment vessel in the irradiating of electrons.

4. A manufacturing method of a flat panel display as set  
forth in claim 2,

25 wherein the electrons emitted from the electron source are  
irradiated, while being deflected, in the irradiating of  
electrons.

5. A manufacturing method of a flat panel display as set  
forth in claim 2,

wherein the electrons emitted from a planar type of the electron source are irradiated in the irradiating of electrons.

6. A manufacturing method of a flat panel display as set forth in claim 1,

5 wherein the electrons are irradiated in a vacuum atmosphere of which degree of vacuum is maintained at  $10^{-3}$  Torr or less in the irradiating of electrons.

7. A manufacturing method of a flat panel display as set forth in claim 1,

10 wherein the electrons are irradiated while heating at least one of the substrate and the faceplate in the irradiating of electrons.

8. A manufacturing method of a flat panel display as set forth in claim 7,

15 wherein at least one of the substrate and the faceplate is heated at a temperature in the range from 200 to 400°C in the irradiating of electrons.

9. A manufacturing method of a flat panel display as set forth in claim 7,

20 wherein, after the electrons are irradiated onto at least one of the substrate and the faceplate, an irradiated object is cooled to a temperature of 100°C or less.

10. A manufacturing method of a flat panel display as set forth in claim 1,

25 wherein the substrate and the faceplate are joined through a supporting frame in a vacuum atmosphere after the electrons are irradiated onto at least one of them.

11. A manufacturing method of a flat panel display as set

forth in claim 10,

wherein the supporting frame is irradiated with electrons in the irradiating of electrons.

12. A manufacturing method of a flat panel display as set  
5 forth in claim 1,

wherein the irradiating of the substrate and the irradiating of the faceplate are carried out in the same treatment vessel.

13. Manufacturing equipment of a flat panel display,  
10 comprising:

a treatment vessel in which at least one of a substrate, which has an electron emitting element, and a faceplate, which has a phosphor screen, is accommodated;

transferring means for sending at least one of the substrate  
15 and the faceplate in and out of the treatment vessel;

exhausting means for evacuating the inside of the treatment vessel to a vacuum atmosphere;

irradiating means for irradiating an electron beam onto at least one of the substrate and the faceplate, which are  
20 accommodated in the treatment vessel; and

joining means for joining the substrate and the faceplate, at least one of which is irradiated with the electron beam, while arranging so as for the electron emitting element and the phosphor screen to face to each other with a gap.

14. Manufacturing equipment of a flat panel display as set  
25 forth in claim 13, further comprising:

means for heating at least one of the substrate and the faceplate, which are accommodated in the treatment vessel.



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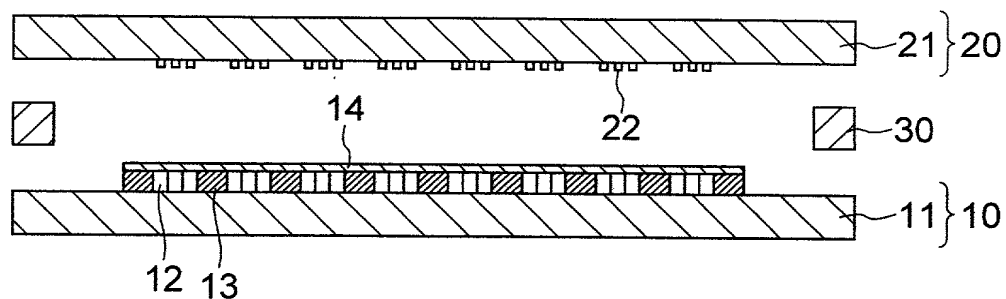
DOCKET # 215225US2PT SHEET 1 OF 4

FIG. 1A

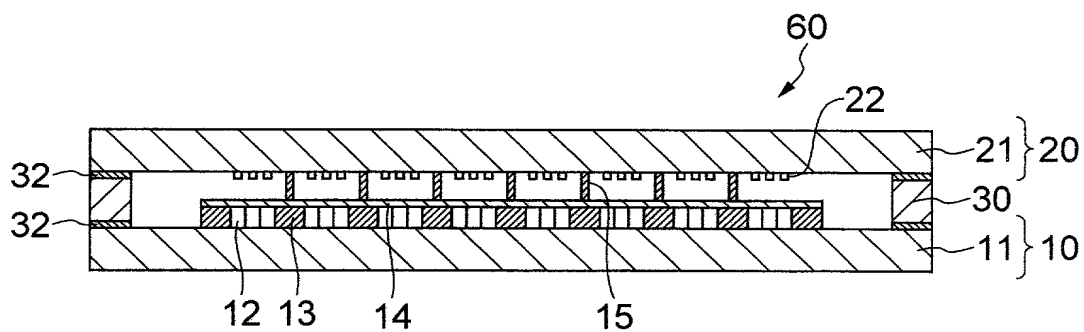


FIG. 1B

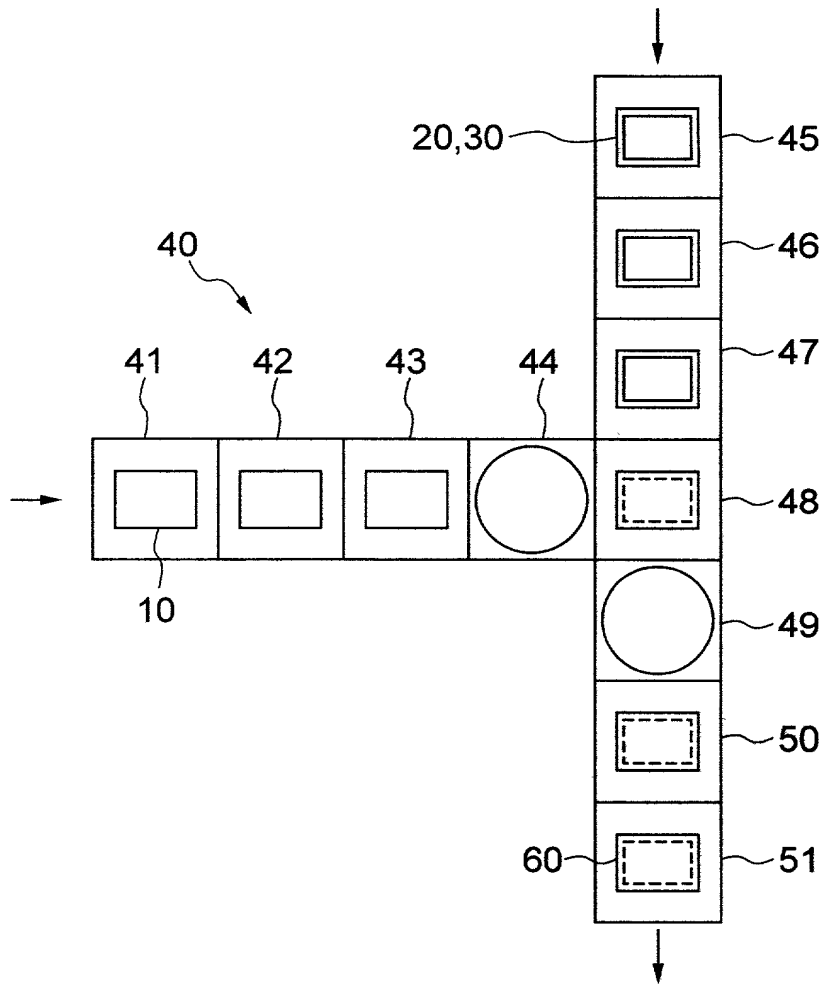


FIG. 2

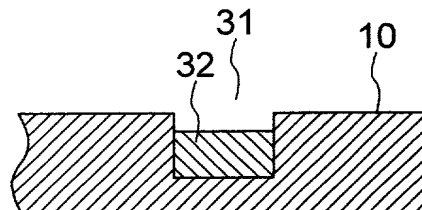


FIG. 3

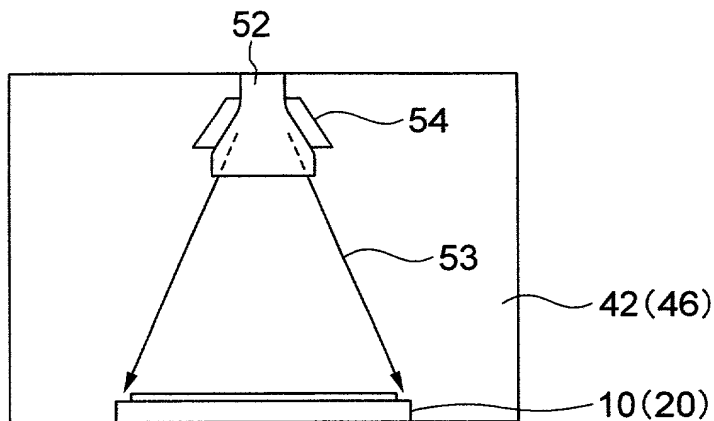


FIG. 4

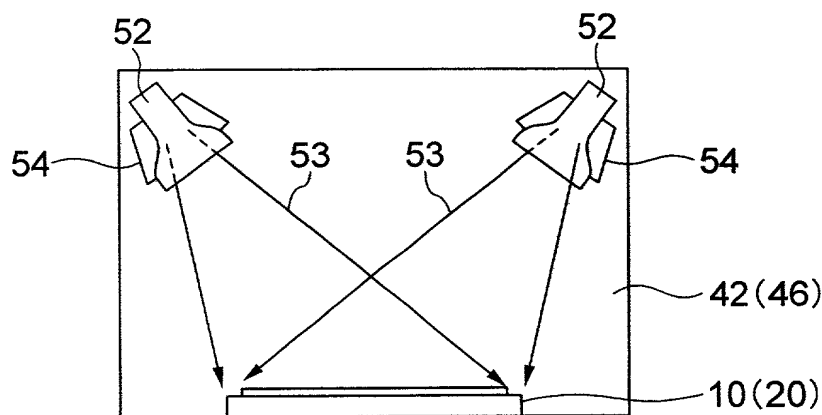


FIG. 5

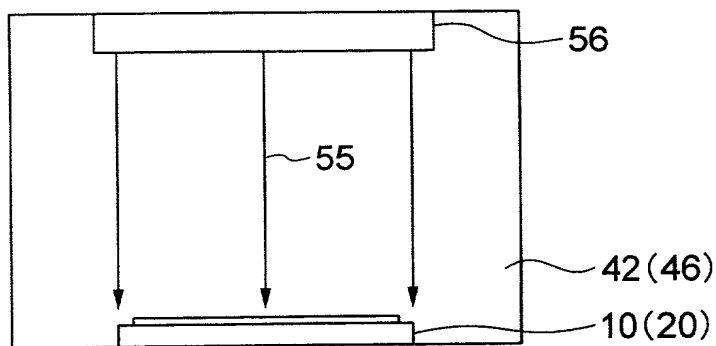


FIG. 6

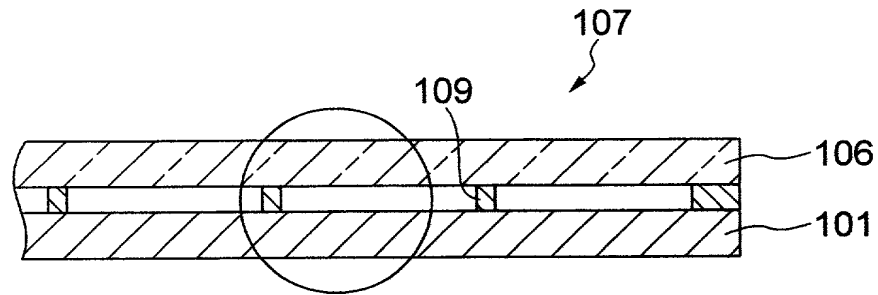


FIG. 7A

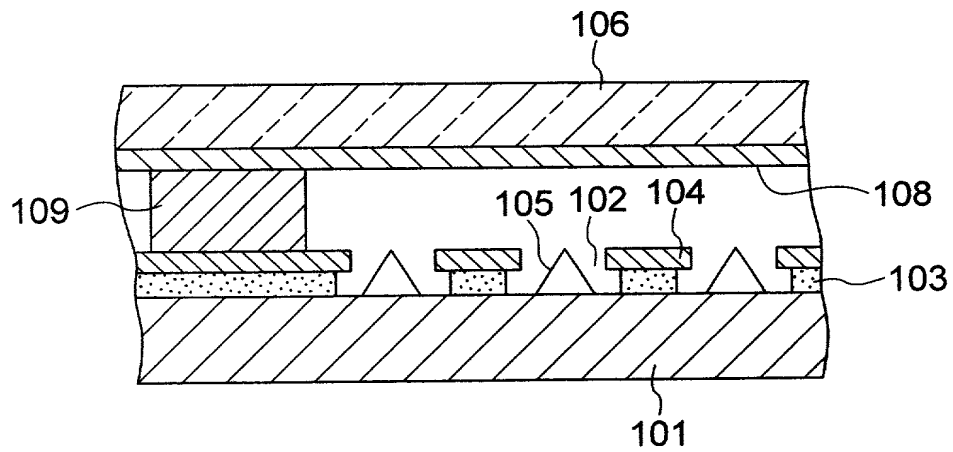


FIG. 7B

# Declaration and Power of Attorney For Patent Application

## 特許出願宣言書及び委任状

### Japanese Language Declaration

#### 日本語宣言書

下記の氏名の発明者として、私は以下の通り宣言します。

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者（下記の名称が複数の場合）であると信じています。

上記発明の明細書は、

☐ 本書に添付されています。

☒ 月 日 に提出され、米国出願番号または特許協定条約国際出願番号を \_\_\_\_\_ とし、  
(該当する場合) \_\_\_\_\_ に訂正されました。

私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

私は、連邦規則法典第37編第1条56項に定義されるとおり、特許資格の有無について重要な情報を開示する義務があることを認めます。

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled.

MANUFACTURING METHOD OF FLAT PANEL

DISPLAY, AND MANUFACTURING EQUIPMENT

OF FLAT PANEL DISPLAY

the specification of which

☐ is attached hereto.

☒ was filed on April 24, 2000  
as United States Application Number or  
PCT International Application Number  
PCT/JP00/02658 and was amended on  
\_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

Japanese Language Declaration  
(日本語宣言書)

私は、米国法典第35編119条 (a) - (d) 項又は365条 (b) 項に基づき下記の、米国以外の国の少なくとも一カ国を指定している特許協力条約365 (a) 項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している、本出願の前に出願された特許または発明者証の外国出願を以下に、枠内をマークすることで、示しています。

Prior Foreign Application(s)  
外国での先行出願

P11-122220

(Number)  
(番号)

Japan

(Country)  
(国名)

(Number)  
(番号)

(Country)  
(国名)

私は、第35編米国法典119条 (e) 項に基づいて下記の米国特許出願規定に記載された権利をここに主張いたします。

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、下記の米国法典第35編120条に基づいて下記の米国特許出願に記載された権利、又は米国を指定している特許協力条約365条 (c) に基づく権利をここに主張します。また、本出願の各請求範囲の内容が米国法典第35編112条第1項又は特許協力条約で規定された方法で先行する米国特許出願に開示されていない限り、その先行米国出願書提出日以降で本出願書の日本国内または特許協力条約国際提出日までの期間中に入手された、連邦規則法典第37編1条56項で定義された特許資格の有無に関する重要な情報について開示義務があることを認識しています。

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

私は、私自信の知識に基づいて本宣言書中で私が行なう表明が真実であり、かつ私の入手した情報と私の信じることに基づく表明が全て真実であると信じていること、さらに故意になされた虚偽の表明及びそれと同等の行為は米国法典第18編第1001条に基づき、罰金または拘禁、もしくはその両方により処罰されること、そしてそのような故意による虚偽の声明を行なえば、出願した、又は既に許可された特許の有効性が失われることを認識し、よってここに上記のごとく宣誓を致します。

I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

Priority Claimed  
優先権主張

28/April/1999

(Day/Month/Year Filed)  
(出願年月日)

☒

Yes  
はい

☐

No  
いいえ

(Day/Month/Year Filed)  
(出願年月日)

☐

Yes  
はい

☐

No  
いいえ

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.)  
(出願番号)

(Filing Date)  
(出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、係属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Japanese Language Declaration  
(日本語宣言書)

委任状：私は下記の発明者として、本出願に関する一切の手続きを米特許商標局に対して遂行する弁理士または代理人として、下記の者を指名いたします。  
(弁護士、または代理人の指名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: (list name and registration number)



022850

書類送付先

Send Correspondence to:



022850

直接 連絡先：(名前及び電話番号)

Direct Telephone Calls to: (name and telephone number)  
(703) 413-3000

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	Citizenship Japan
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日付	Date Oct. 15, 2001
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(第三以降の共同発明者についても同様に記載し、署名すること)

(Supply similar information and signature for third and subsequent joint inventors.)